

SANYO Semiconductors

DATA SHEET

P-Channel Silicon MOSFET

ATP114 — General-Purpose Switching Device Applications

Features

- ON-resistance RDS(on)1=12m Ω (typ.)
- · 4V drive

- Input Capacitance Ciss=4000pF(typ.)
- · Halogen free compliance

Specifications

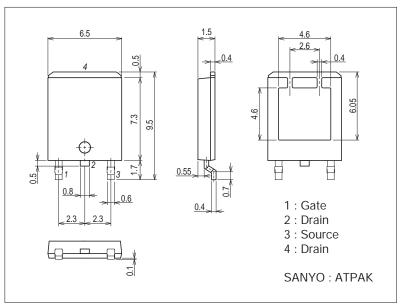
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-60	V
Gate-to-Source Voltage	V _{GSS}		±20	V
Drain Current (DC)	ID		-55	А
Drain Current (PW≤10μs)	I _{DP}	PW≤10μs, duty cycle≤1%	-165	А
Allowable Power Dissipation	PD	Tc=25°C	60	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		100	mJ
Avalanche Current *2	IAV		-28	Α

Note :*1 V_{DD}=-15V, L=200μH, I_AV=-28A

Package Dimensions

unit : mm (typ) 7057-001



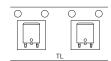
Product & Package Information

Package : ATPAKJEITA, JEDEC : -

JEIII, JEDEC

• Minimum Packing Quantity : 3,000 pcs./reel

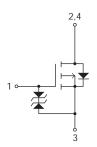
Packing Type: TL



Marking



Electrical Connection

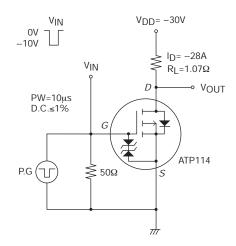


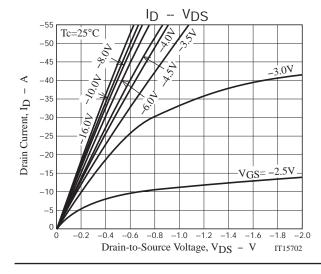
^{*2} L≤100µH, Single pulse

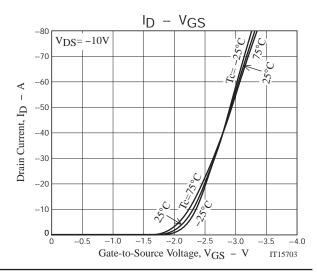
Electrical Characteristics at Ta=25°C

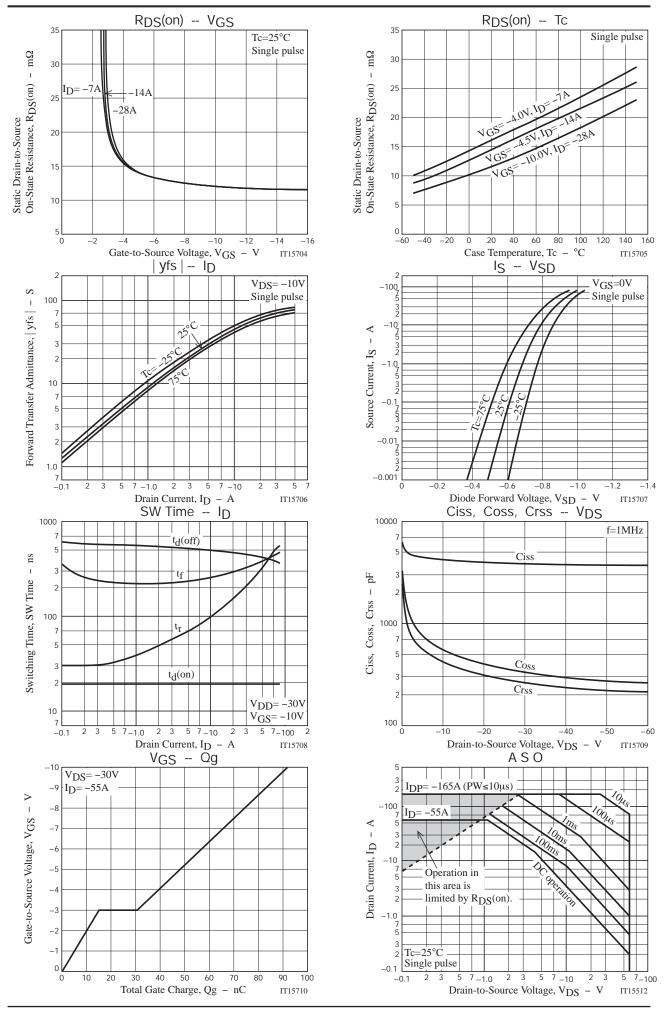
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-60V, V _{GS} =0V			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	VDS=-10V, ID=-28A		65		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =-28A, V _G S=-10V		12	16	mΩ
	R _{DS} (on)2	I _D =-14A, V _G S=-4.5V		15	21	mΩ
	R _{DS} (on)3	I _D =-7A, V _G S=-4V		16.5	24	mΩ
Input Capacitance	Ciss	V _{DS} =-20V, f=1MHz		4000		pF
Output Capacitance	Coss	V _{DS} =-20V, f=1MHz		400		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-20V, f=1MHz		315		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		19		ns
Rise Time	tr	See specified Test Circuit.		200		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		450		ns
Fall Time	tf	See specified Test Circuit.		300		ns
Total Gate Charge	Qg	V _{DS} =-30V, V _{GS} =-10V, I _D =-55A		92		nC
Gate-to-Source Charge	Qgs	V _{DS} =-30V, V _{GS} =-10V, I _D =-55A		15		nC
Gate-to-Drain "Miller" Charge	Qgd	V _D S=-30V, V _G S=-10V, I _D =-55A		15.5		nC
Diode Forward Voltage	V _{SD}	I _S =-55A, V _G S=0V		-0.95	-1.5	V

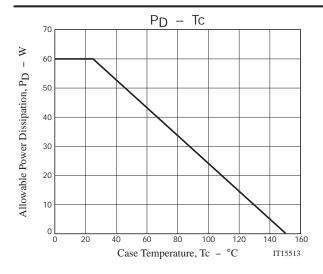
Switching Time Test Circuit

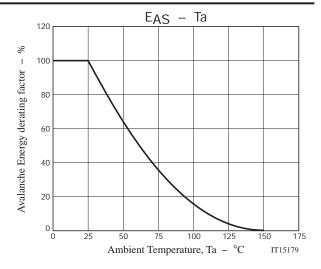












Note on usage: Since the ATP114 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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